

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Lu) I hereby certify that this paper is
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Serial No.: 10/619,348) States Postal Service with
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Filed: July 14, 2003) mail in an envelope addressed to:
) Commissioner for Patents, P.O.
Assignee: Intel Corporation) Box 1450, Alexandria, VA 22313-
) 1450 on this date:
For: Optical Packages And Methods For) 12/2/2
Controlling A Standoff Height In Optical) DATED: 12-123/03
Packages	2 (671/1
Cross Art Units 2074) Jan le Thyl
Group Art Unit: 2874) James A. Flight
Parasita and III 1	Registration No. 37,622
Examiner: Unknown) * Attorney for Applicant

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The patents and/or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 CFR §§ 1.56, 1.97, and 1.98. Copies of the patents or publications are enclosed.

TIME OF FILING

This information disclosure statement is being filed to the best of the undersigned's knowledge, before the mailing date of a first Office action on the merits. In accordance with 37 CFR §1.97(b), no certification or fee is required.

METHOD OF PAYMENT

No fee is required.

The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 50-2455. A copy of this paper is enclosed.

Correspondence Address:

Respectfully submitted,

GROSSMAN & FLIGHT, LLC. 20 N. Wacker Drive Suite 4220

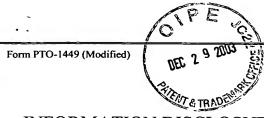
Chicago, Illinois 60606 (312) 580-1020

DATED: 12/23/03

By: James A. Flight

Registration No.: 37,622

Attorneys for Intel Corporation



U.S. Department of Commerce Patent and Trademark Office Atty. Docket No. Serial No.
INTEL/16653 10/619,348

Applicant
Lu

Filing Date Group Art Unit

2874

July 14, 2003

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

			U.S. PATE	ENT DOCUMENTS			
*EXAMINER INITIALS		DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A01						

		F	OREIGN PAT	TENT DOCU	MENTS			
*Examiner		Document	Publication	Country	Class	Subclass	Translat	ion
Initials		Number	Date				Yes	No
	B01							

	OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)
C01	Li et al., Wafer Level and Flip Chip Design Through Solder Prediction Models and Validation, Proceedings of the 7 th Mechatronics Forum International Conference (September, 2000).
C02	VCSEL (Vertical Cavity Surface Emitting Laser), http://www.lasermate.com (printed February 24, 2003).
C03	VCSEL - A Whatis Definition, http://www.techtarget.com (printed February 24, 2003).

EXAMINER

DATE CONSIDERED

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not cinsidered. Include copy of this form with next communication to applicant.